

FIG. 2

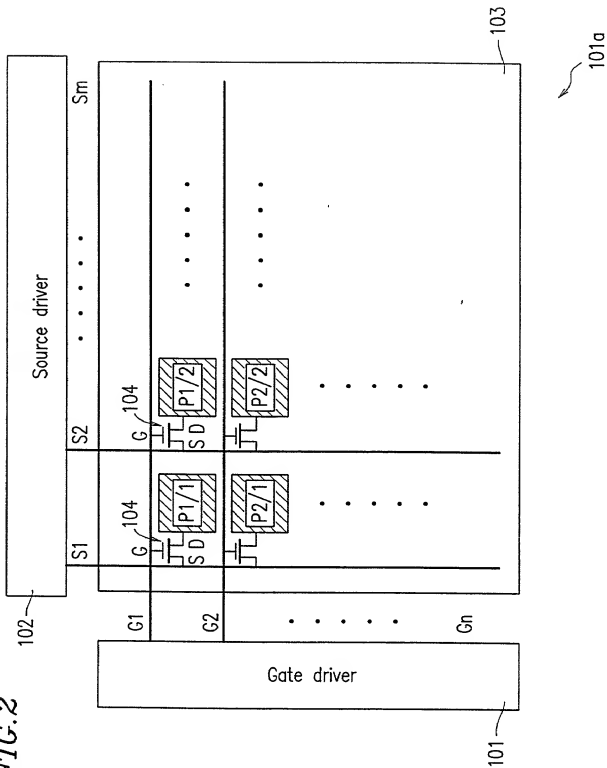


FIG. 3

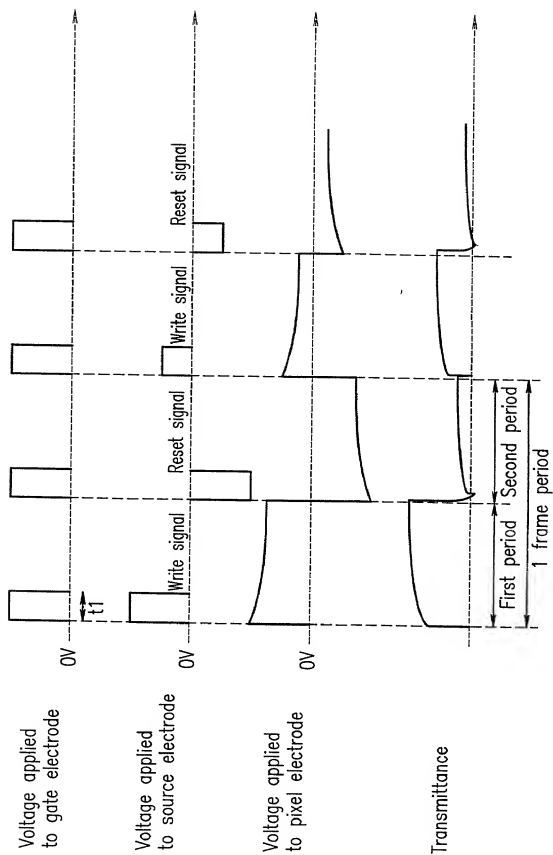
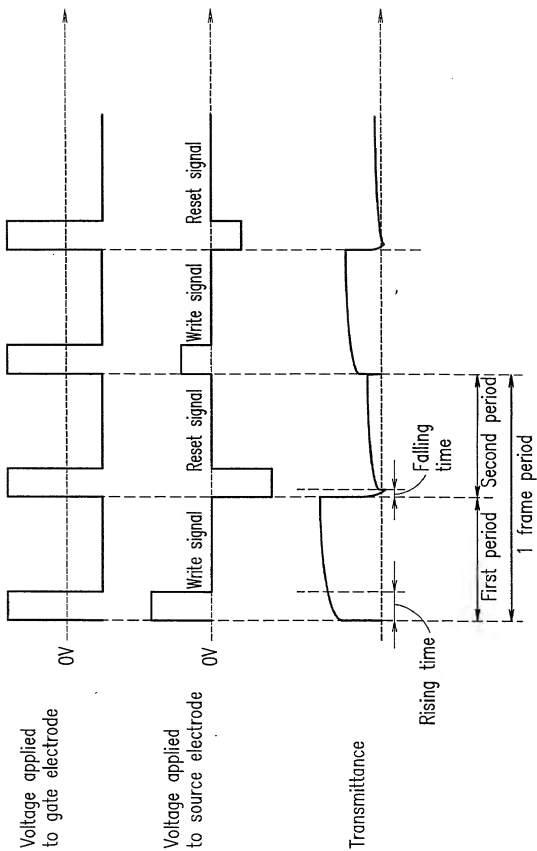




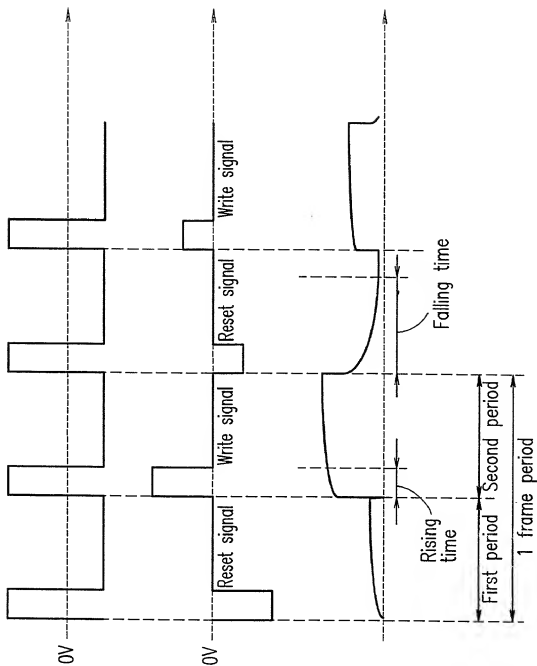
FIG. 5



Voltage applied  
to gate electrode

Voltage applied  
to gate electrode

Voltage applied  
to source electrode



Transmittance

Axis when the maximum  
reset signal is applied

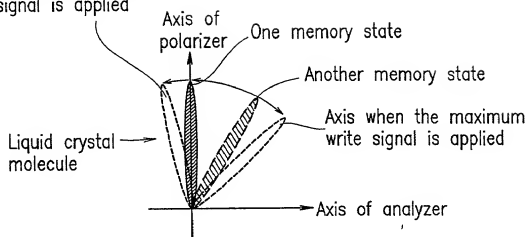


FIG. 8

